M.Sc.
Microelectronics and Chip Design
(Starting from the Winter Semester 2025/26)

2 Core Elective Modules

- **Analog/Mixed-Signal Design**
- **Digital Design**

(20 CP; 15 CP must be from one core elective and 5 CP from the other core elective)

Elective Module: Specialized Elective Course
(30 CP)

Elective Module: Interdisciplinary Course
(10 CP)

Master's Thesis
(30 CP)

Scientific Seminar
(5 CP)

Research Laboratory Functional Design – Part I
(10 CP)

Research Laboratory Physical Design – Part II
(5 CP)

Fundamental Laboratory Course
(5 CP)

Chip Design Test and Evaluation Laboratory Course
(5 CP)

Semester 1 (WiSe, 30 CP)	Semester 2 (SoSe, 30 CP)	Semester 3 (WiSe, 30 CP)	Semester 4 (SoSe, 30 CP)